

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant: Chambers

Art Unit: 2829

Serial No.: 10/649,425

Examiner: Sarkar, A.

APR 20 2005

Filing Date: 08/27/2003

Docket No.: TI-35714

Customer No.: 23494

Conf. No.: 5169

Title: STRUCTURE AND METHOD TO FABRICATE SELF-ALIGNED
TRANSISTORS WITH DUAL WORK FUNCTION METAL GATE ELECTRODESREQUEST FOR EXTENSION OF TIMECommissioner of Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by
facsimile to the U.S. Patent and Trademark Office at 703-872-9306 on
the date shown below:
Jacqueline J. Garner, Reg. No. 36,144April 20, 2005
Date

Pursuant to 37 CFR 1.136(a), Applicant(s) respectfully petitions the Commissioner for an extension of the shortened statutory period for response in the above-identified Application.


The fee for this extension is indicated below:

- ☐ One Month (\$120)
☐ Two Months (\$450)
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Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, TX 75265
(214) 532-9348
Fax: (972) 917-4418

Respectfully Submitted,


Jacqueline J. Garner
Reg. No. 36,144

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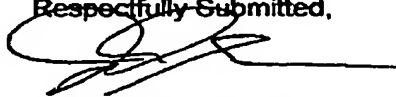
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